

to any paragraphs in Subramanian II. While Subramanian I and Subramanian II are each generally concerned with compatibility between protocols, they do not anywhere teach a channel CODEC having processor core providing a kernel block with configuration data, not to mention configuration data which causes the kernel block to perform a step of a channel CODEC algorithm according to one of a plurality of wireless communication standards as specified by the configuration data. Applicant therefore respectfully requests the Examiner to either specifically refer to paragraphs or portions of Subramanian I and Subramanian II where he believes this feature to be taught, or withdraw the prior art rejections.

In view of the above, Applicant believes the pending application is in condition for allowance.

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Respectfully submitted,

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